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PRINCETION NO	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
APPLICATION NO.		Francis G. Celii T1-29270	TI-29276	8761	
09/599,718	06/22/2000	Francis G. Cein			
75'	an 10/17/2002				
Jacqueline J Garner			EXAMINER		
Jacqueline J G	arner		DROUGH CHARLOTTE A		
Texas Instruments Inc			BROWN, CHARLOTTE A		
PO Box 655474	<b>,</b>				
MS 3999			ART UNIT	PAPER NUMBER	
Dallas, TX 752	265		1765	9	
			DATE MAILED: 10/17/2002	DATE MAILED: 10/17/2002	

Please find below and/or attached an Office communication concerning this application or proceeding.

Application No. 09/599,718

Applicant

MKS

· Office Action Summary

Examiner

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Kraft et al.

·	Office Addion Campany,	Charlotte Brown	1765	
	The MAILING DATE of this communication appears	on the cover sheet with the corre	spondence addres	s
THE N - Extensi mailing - If the p - If NO p - Failure	DRTENED STATUTORY PERIOD FOR REPLY IS SE- MAILING DATE OF THIS COMMUNICATION.  Joins of time may be available under the provisions of 37 CFR 1.136 (a). It date of this communication.  Joins of time may be available under the provisions of 37 CFR 1.136 (a). It date of this communication.  Joins of time may be available under the provisions of 37 CFR 1.136 (a). It date of this communication.  Joins of time may be available under the maximum statutory period will apply to reply within the set or extended period for reply will, by statute, cause the received by the Office later than three months after the mailing date or	n no event, however, may a reply be timely file the statutory minimum of thirty (30) days will y and will expire SIX (6) MONTHS from the mai	d after SIX (6) MONTHS be considered timely. ling date of this commun .S.C. § 133).	
earned	patent term adjustment. See 37 CFR 1.704(b).			
Status	Responsive to communication(s) filed on Jul 29,	2002		· .
1) 🔯	This a	ction is non-final.		
2a) 💢 3) □	This action is there.	a except for formal matters, pros	ecution as to the 3 O.G. 213.	e merits is
Dispos				application.
4) 💢	Ition of Claims  Claim(s) 1-14	15/6	ne pending in the	esseideration
	4-) Of the above claim(s)	15/	ale Withdrawn	
5) 🗆	Claim(a)		13/6/0 0/10/1001	
			is/are rejected	
6) 🗶	Claim(s)		is/are objected	d to.
7) 🗆	Claim(s)	are subject to res	triction and/or ele	ection requirement.
8) 🗆				
Applic	cation Papers	•		
9)	The specification is objected to by the Examiner  The drawing(s) filed on is,	/are a\□ accepted or b)□ obje	cted to by the E	xaminer.
10)[				
	Applicant may not request that any objection to the The proposed drawing correction filed on	is: a) approv	ed b)□ disappro	ved by the Examiner
11)[	The proposed drawing correction filed on	aby to this Office action.		
	If approved, corrected drawings are required in re	cominer		
12)[		(allillio)		
Prior	ity under 35 U.S.C. §§ 119 and 120  Acknowledgement is made of a claim for foreig	on priority under 35 U.S.C. § 11	9(a)-(d) or (f).	
13)[	Acknowledgement is made of a claim for follows	go poors,		
8	a) ☐ All b) ☐ Some* c) ☐ None of:	have heen received.		
	<ul> <li>1.  Certified copies of the priority documents</li> <li>2.  Certified copies of the priority documents</li> </ul>	have been received in Applicati	on No	
	and the prior	ity documents have been receive	ed in this Nationa	ıl Stage
	3. Copies of the certified copies of the prior application from the International	Bureau (PCT Rule 17.2(a)).		
	To the attached detailed Office action for a list	Of the certified cobics that the	119(a)	
14)	- Astronologoment is made of a claim for dom	estic priority under 35 0.3.C. s	110(0).	
i i	tul terries language DrOVI	isinnai andiication nas scon rocci		١.
15)	a) The translation of the foreign language provided Acknowledgement is made of a claim for domination and the control of the foreign language provided the control of the c	nestic priority under 35 0.5.C. 3	) 120 UNO/OF 12	
Atta	chment(s)	4) Interview Summary (PTO-413)		
1) [	Notice of References Cited (PTO-892)	5) Notice of Informal Patent Appl		
2) [	Notice of Draftsperson's Patent Drawing Review (PTO-948)	6) Other:		
3)	Information Disclosure Statement(s) (PTO-1449) Paper No(s).			

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## **DETAILED ACTION**

- 1. Applicant's arguments with respect to claims 1-14 have been considered but are moot in view of the new ground(s) of rejection.
- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1-14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Jang et al. (US 6,019,906) in view of Grill et al. (US 6,140,226) and further in view of Chen et al. (US 6,319,822).

Jang teaches a hard masking method for forming a patterned microelectronics layer within a microelectronics fabrication. A semiconductor substrate is provided. A blanket first dielectric layer is formed on the semiconductor substrate. The dielectric layer may be formed from any of several dielectric materials that are conventional in the art (Column 10, lines 46-50). This reads on the applicant's limitation of forming an interlevel dielectric layer over a semiconductor body. Patterned conductor layers are formed over the blanket first dielectric layer. A blanket inter-metal dielectric layer is formed over the substrate. The layer is formed from an oxygen containing plasma etchable material which is a low dielectric constant material.

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The materials may include but are not limited to organic polymer spin-on-polymer dielectric materials (Column 11, lines 32-50). A hard mask layer is formed over the structure. A series of patterned photoresist layers are formed (Column 12, lines 12-30). This reads on the applicant's limitation of forming a via pattern over the hard mask. A first plasma etch is employed to etch through the hard mask layer (Column 12, lines 51-57). A second plasma etch is performed to etch the blanket inter-metal dielectric layer (Column 13, lines 7-20). This reads on the applicant's limitation of extending the via by selectively etching the intrametal dielectric layer.

Unlike the claimed invention, Jang does not teach methods for depositing a BARC layer over the hard mask and within the via, forming a trench pattern over the BARC layer, and etching a trench in the intrametal dielectric layer.

Grill discloses a dual damascene process. A semiconductor base is provided. A dielectric passivation layer is deposited over the semiconductor base. This reads on the applicant's limitation of forming an interlevel dielectric layer over a semiconductor body. An optional dielectric etch stop layer is deposited over the passivation layer. This reads on the applicant's limitation of forming a shelf layer over the interlevel dielectric layer. A thin layer of conductive or insulating material is deposited over a hardmask and in a via. Possible conductive and insulating materials include TaN, TiN, and HfN (Column 5, lines 43-61). Titanium nitride (TiN) is a BARC layer (See Padmanaban et al., Column 2, lines 1-3). This reads on the applicant's limitation of depositing a BARC layer over the hardmask within the via. A trench pattern is formed over the BARC layer (Figure 3F). A trench is etched in the dielectric layer. Portions of

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the BARC layer are then removed before fabrication of any overlying wiring or via levels (Column 6, lines 6-14). Figure 3G of the drawings shows that the via is extended by selectively etching the intrametal dielectric layer (8) and the shelf layer (7).

It is the Examiner's position that a person having ordinary skill in the art would have found it obvious to modify Jang with the methods of depositing a BARC layer over the hard mask and within the via, forming a trench pattern over the BARC layer, and etching a trench in the intrametal dielectric layer as taught by Grill. These additional steps would have been anticipated in order to from a trench in the intrametal dielectric layer.

Unlike the claimed invention, neither Jang nor Grill teach a method for depositing a BARC layer over the hardmask and within the via wherein the BARC layer is significantly thicker within the via than over the hardmask.

Chen discloses a process for forming an integrated contact or via. A TiN hardmask is formed over the via (Column 5, lines 11-20). A barrier layer of TiN is deposited over hardmask (Column 5, lines 48-51). This reads on the applicant's limitation of depositing a BARC layer over a hardmask and within a via. Figure 6 of the drawings shows that the BARC layer is significantly thicker within the via than over the hardmask.

It is the Examiner's position that a person having ordinary skill in the art at the time of the claimed invention would have found it obvious to modify Jang and Grill with the method of providing a thicker BARC layer within the via as taught by Chen. The additional step of

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providing a thicker BARC within the via as taught by Chen would have been anticipated in order to form an integrated circuit or via in order to achieve a reasonable expectation of success.

4. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL.** See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

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5. Any inquiry concerning this communication from the Examiner should be directed to Charlotte A. Brown whose telephone number is (703) 305-0727. The Examiner can normally be reached during the hours of 9:00AM to 6:30PM.

The fax phone numbers where this application or proceeding is assigned are 703-872-9310 for regular communications and 703-872-9311 for After Final communications.

CAB

October 15, 2002

BENJAMIN L. UTECH SUPERVISORY PATENT EXAMINER